

# DSL2019

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## ***Prof. Francesca Iacopi, PhD***

Materials Scientist and Nanoelectronics expert with 20 years' experience in semiconductor Industry and Academia, she is author of over 120 peer-reviewed publications and holder of 9 US granted patents. Her research emphasis is in the translation of basic scientific advances in nanomaterials and novel device concepts into semiconductor technologies. In particular, her seminal work on low-k dielectrics for interconnects over the 1999-2009 decade has guided the industrial uptake of porous dielectrics into modern semiconductor microchips.

Research Scientist at IMEC (Belgium) over 1999-2009, she then took up a year Guest Professorship at the University of Tokyo (Japan). In 2010-2011 she directed the Chip-Package Interaction strategy for GLOBALFOUNDRIES (Ca, USA), the world's second -largest semiconductor foundry. At Griffith University (Australia) she invented a process to obtain graphene on silicon, with applications in integrated microtechnologies, such as bio-compatible sensing and energy storage. At the University of Technology Sydney since 2016, she is Head of Discipline, Communications and Electronics, in the Faculty of Engineering & IT, and leader of the Integrated Nano Systems Lab, with focus on smart sensor nodes for the Internet of Things.

Major awards include a Gold Graduate Student Award from the Materials Research Society (2003, USA), a Future Fellowship from the Australian Research Council (2012-2016), a Global Innovation Award (2014, Washington DC). She was one of the 16 founding members of the Advance Queensland Panel of Experts, advisory committee to the State Government on Science and Innovation. She is a Fellow of the Institute of Engineers Australia and Senior Member of IEEE, volunteering on a regular basis for both IEEE and MRS.